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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.	Serial No.			
				M-3240-3D US	Not yet assigned			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant				
(Use several sheets if necessary)				Coke S. Reed				
				Filing Date	Group			
				Herewith	2661			
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
<i>lyn</i>	AA	4,814,980	3/1989	Peterson et al.	395	200.46		
<i>lyn</i>	AB	4,933,836	6/1990	Tulpule et al.	395	800.12		
<i>lyn</i>	AC	5,181,017	1/1993	Frey, Jr. et al.	340	825.02		
<i>lyn</i>	AD	5,253,248	10/1993	Dravida et al.	370	228		
<i>lyn</i>	AE	5,339,396	8/1994	Muramatsu et al.	395	312		
<i>lyn</i>	AF	5,377,333	12/1994	Nakagoshi et al.	395	312		
<i>lyn</i>	AG	5,471,623	11/1995	Napolitano, Jr.	395	200.03		
<i>lyn</i>	AH	5,546,596	8/1996	Geist	395	200.73		
<i>lyn</i>	AI	5,553,078	9/1996	Horie	370	389		
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<i>lyn</i>	AK	5,583,990	12/1996	Birrittella et al.	395	800.29		
<i>lyn</i>	AL	5,606,551	2/1997	Kartalopoulos	370	406		
<i>lyn</i>	AM	5,617,413	4/1997	Monacos	370	400		
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Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
<i>lyn</i>	AO	94 12939	6//1994	WIPO				
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<i>lyn</i>	AQ	Aruna V. Ramanan, "Ultrafast Space-Time Networks for Multiprocessors", a thesis, 1993, pp. 1-170						
<i>lyn</i>	AR	Malek M., et al.: "The Cylindrical Banyan Multicomputer: A Reconfigurable Systolic Architecture", May 1, 1989, pp. 319-327, Parallel Computing, XP000065558						
<i>lyn</i>	AS	Isaac Yi-Yuan Lee et al.: "A Versatile Ring-Connected Hypercube", Jun. 1, 1994, pp. 60-67, IEEE Micro., pp. 60-67, XP000448657						
Examiner <i>lyn</i>		Date Considered		8/15/05				
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								

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Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
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<i>HN</i>	AJ	Narashima Reddy: "I/O Embedding in Hypercubes", Aug. 19, 1988, pp. 331-338, Proceedings of the 1988 Intern'l Conf. on Parallel Processing, Pennsylvania State Univ., XP002016775						
<i>HN</i>	AK	Catier: "Une architecture "hypercube".", Sep. 1986, pp. 59-64, Electronique Industrielle, XP002016776						
<i>HN</i>	AL	Welty: "Hypercube architectures", Jun. 19, 1986, pp. 495-501, AFIPS Conference Proceedings 1986 National Computer Conference, XP002016777						
<i>MA</i>	AM	Proceedings of the Third IEEE Symposium on Parallel and Distributed Processing (Cat. No. 91TH0396-2), Dallas, TX, USA, Dec. 2-5, 1991, ISBN 0-8186-2310-1, Los Alamitos, CA, USA, IEEE Compt. Soc. Press, USA, pp. 564-571						
<i>AN</i>	AN	Young, S.D., et al, "Adaptive Routing in Generalized Hypercube Architectures", IEEE Symposium, Dec. 2-5, 1991, pp. 564-571, XP002024983						
<i>HN</i>	AO	Gaughan, P.T., et al.: "Adaptive Routing Protocols for Hypercube Interconnection Networks", Computer, vol. 26, No. 5, May 1, 1993, pp. 12-16, 17-23, XP000365279						
Examiner <i>Parikh DR</i>		Date Considered <i>2/15/03</i>						
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								